

产品规格书

Specifcation of products

产品名称:可控硅模块

产品型号: MTG250A-T22

浙江世菱半导体有限公司
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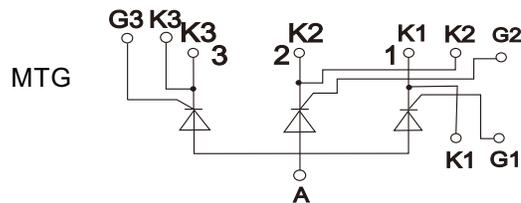
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| 拟制 | 审核 | 核准 |
| 林益龙 | 曹剑龙 | 宗瑞 |

| SYMBOL | CHARACTERISTIC | TEST CONDITIONS | T _j (°C) | VALUE | | | UNIT |
|--------------------------------------|--|---|---------------------|-------|------|-------|----------------------------------|
| | | | | Min | Type | Max | |
| I _{T(AV)} | Mean on-state current | 180° half sinewave 50Hz Single side cooled, T _c =90°C | 125 | | | 250 | A |
| I _{T(RMS)} | RMS on-state current | Single side cooled, T _c =90°C | 125 | | | 393 | A |
| V _{DRM} V _{RRM} | Repetitive peak off-state voltage Repetitive peak reverse voltage | V _{DRM} &V _{RRM} tp=10ms V _{DSM} &V _{RSM} = V _{DRM} &V _{RRM} +200V respectively | 125 | 800 | | 1800 | V |
| I _{DRM} I _{RRM} | Repetitive peak current | at V _{DRM} at V _{RRM} | 125 | | | 20 | mA |
| I _{TSM} | Surge on-state current | 10ms half sine wave | 125 | | | 8.50 | KA |
| I ² t | I ² T for fusing coordination | V _R =60%V _{RRM} | | | | 368 | A ² s*10 ³ |
| V _{TO} | Threshold voltage | | 125 | | | 0.80 | V |
| r _T | On-state slop resistance | | | | | 1.02 | m Ω |
| V _{TM} | Peak onstate voltage | I _{TM} =750A | 125 | | | 1.4 | V |
| dv/dt | Critical rate of rise of off-state voltage | V _{DM} =67%V _{DRM} | 125 | | | 800 | V/ s |
| di/dt | Critical rate of rise of on-state current | From 67%V _{DRM} to 750A, Gate source 1.5A t _r ≤ 0.5 μ s Repetitive | 125 | | | 100 | A/ s |
| I _{GT} | Gate trigger current | | 25 | 30 | | 150 | mA |
| V _{GT} | Gate triggervoltage | V _A =12V, I _A =1A | | 1.0 | | 2.5 | V |
| I _H | Holding current | | | 20 | | 100 | mA |
| V _{GD} | Non-trigger gatevoltage | At 67%V _{DRM} | 125 | | | 0.2 | V |
| R _{th(j-c)} | Thermal resistance Junction to heatsink | Single side cooled | | | | 0.100 | °C /W |
| F _m | Thermal connection torque (M6) | | | | 5.0 | | N.m |
| | Mounting torque (M6) | | | | 5.0 | | N.m |
| T _{stg} | Stored temperature | | | -40 | | 140 | °C |
| W _t | Weight | | | | 340 | | g |
| Outline | | | | | | | |

OUTLINE DRAWING & CIRCUIT DIAGRAM



Rating and Characteristic

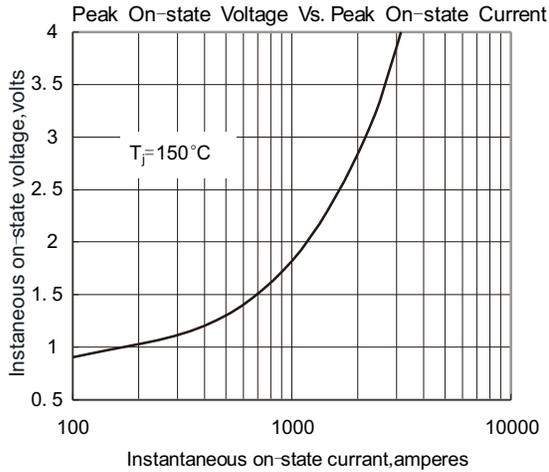


Fig. 1

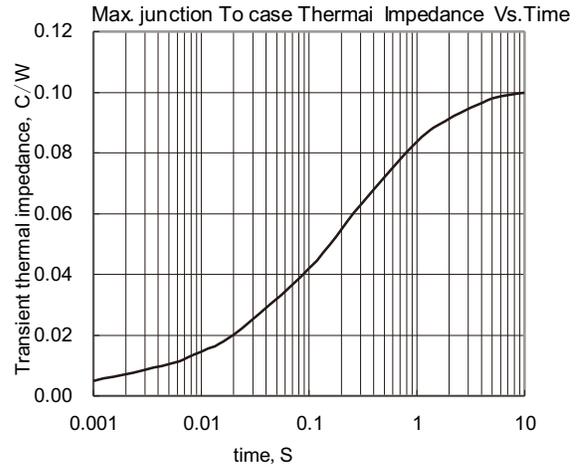


Fig. 2

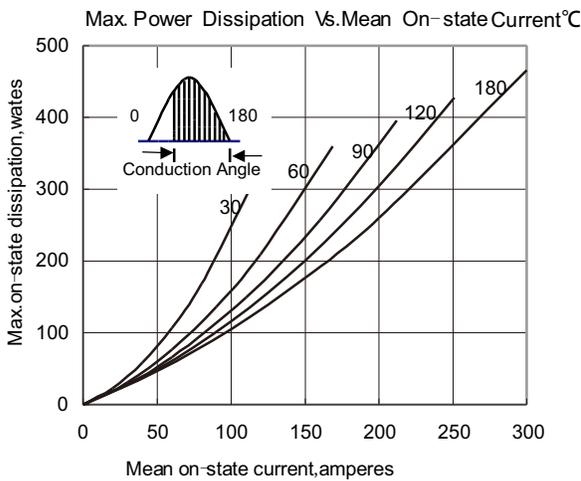


Fig. 3

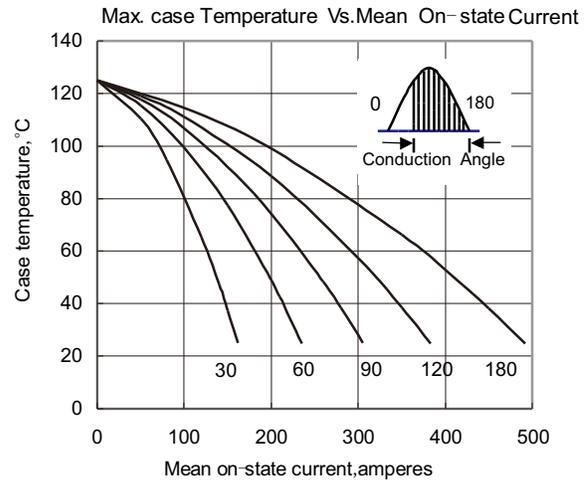


Fig. 4

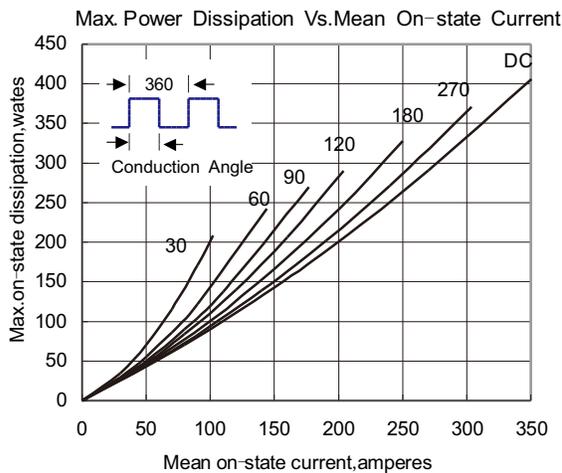


Fig. 5

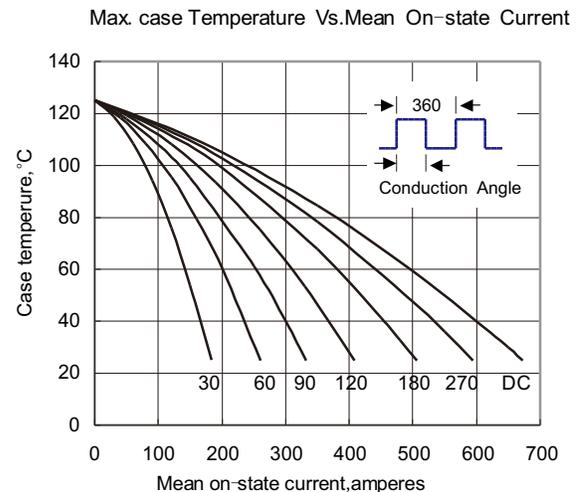


Fig. 6

Outside Dimension

